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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/692,749	10/27/2003	Show-Nan Chung	CHUN3063/EM	8843
23364	7590	07/03/2006		EXAMINER
BACON & THOMAS, PLLC 625 SLATERS LANE FOURTH FLOOR ALEXANDRIA, VA 22314				SHANKAR, VIJAY
			ART UNIT	PAPER NUMBER
			2629	

DATE MAILED: 07/03/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)
	10/692,749	CHUNG ET AL.
	Examiner VIJAY SHANKAR	Art Unit 2629

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) Responsive to communication(s) filed on 27 October 2003.
 2a) This action is FINAL. 2b) This action is non-final.
 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) Claim(s) 1-6 is/are pending in the application.
 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
 5) Claim(s) _____ is/are allowed.
 6) Claim(s) 1 and 3-6 is/are rejected.
 7) Claim(s) 6 is/are objected to.
 8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) The specification is objected to by the Examiner.
 10) The drawing(s) filed on _____ is/are: a) accepted or b) objected to by the Examiner.
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
 Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 a) All b) Some * c) None of:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____ |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____ | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Priority

1. Receipt is acknowledged of papers submitted under 35 U.S.C. 119(a)-(d), which papers have been placed of record in the file.

Drawings

2. Figure 1 should be designated by a legend such as --Prior Art-- because only that which is old is illustrated. See MPEP § 608.02(g). Corrected drawings in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. The replacement sheet(s) should be labeled "Replacement Sheet" in the page header (as per 37 CFR 1.84(c)) so as not to obstruct any portion of the drawing figures. If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

Claim Rejections - 35 USC § 103

3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

4. Claims 1, 3-6 are rejected under 35 U.S.C. 103(a) as being unpatentable over Matthews et al (6,784,855) in view of Zavracky et al (6,121,950).

Regarding Claim 1, Matthews et al teaches an apparatus for controlling switching between a portable device and a docking station (Figures 1-4), comprising: a detection device for generating an installation correctness signal when the portable device is correctly mounted on the docking station (Figures 1-4; Column 3, line 28-Col.5, line 30) ; and a signal selector comprising a first input pin in a connector coupled to a mated connector of the docking station for receiving a control signal from the docking station, (Figures 1-6; Column 3, line 29- Col.7, line 17), and a second input pin for receiving a control signal from the portable device, wherein the signal of the first input pin is selected for output when the signal selector receives the installation correctness signal from the detection device (Figures 1-6; Col. 3, line 29- Col.7, line 17). However, Matthews et al does not teach an apparatus for controlling brightness switching comprising a signal selector comprising a first input pin in a connector coupled to a mated connector of the display device for receiving a brightness control signal from the display device, and a second input pin for receiving a brightness control signal from the portable device, wherein the signal of the first input pin is selected for output when the signal selector receives the installation correctness signal from the detection device.

Zavracky et al teaches an apparatus for controlling brightness switching comprising a signal selector comprising a first input pin in a connector coupled to a mated connector (Col.5, lines 29-47) of the display device for receiving a brightness control signal (55 in fig.1; Col.11, lines 1-40) from the display device, and a second input pin for receiving a brightness control signal (55 in fig.1; Col.11, lines 1-40) from

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the portable device, wherein the signal of the first input pin is selected for output when the signal selector receives the installation correctness signal from the detection device. (Figs.1,10; Col.11, lines 1- Col.12, line 55; Col.16, line 25- Col. 17, line 20).

Thus, it would have been obvious to one of ordinary skill in the art at the time the invention was made to have incorporated the teaching Zavracky et al into Matthews et al for providing better quality display with high resolution.

Regarding Claim 3, Zavracky et al teaches the apparatus wherein the first, second, third, and fifth transistors are N-channel logic level enhancement mode field effect transistor and the fourth transistor is a PNP transistor (Col.14, line 47- Col.15, line 23).

Regarding Claim 4, Zavracky et al teaches the apparatus further comprising an operational amplifying buffer having an input pin and an output pin, the operational amplifying buffer being coupled to the output of the signal selector for increasing the driving capability of the portable device. (Col.12, line 15-45).

Regarding Claims 5-6, Zavracky et al teaches the apparatus further comprising a microprocessor and a digital to analog converter (DAC) (50 in Fig.1), the DAC having an input pin coupled to a output pin of the microprocessor and an output pin coupled to the output pin of the operational amplifying buffer, the microprocessor

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being adapted to execute a program for generating a PWM brightness control signal to control the brightness of the portable device, and wherein the DAC is integrated into the microprocessor (Col.11, line 1- Col.12, line 45; Col.16, line 25- Col.17, line 67).

5. Claim 2 is objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

6. The following is an examiner's statement of reasons for allowance: The prior art fails to teach wherein the signal selector comprises first to fifth resistors, a first capacitor, and first to fifth transistors, the first input pin being connected to the first capacitor, the second resistor, and a gate of the first transistor respectively, the first capacitor being connected to a low level, the second resistor being connected to a high level, a source of the first transistor being at a low level and a drain thereof being coupled to the third resistor and a gate of the second transistor respectively, the third resistor being at a high level, a source of the second transistor being at a low level, the second input pin being coupled to a gate of the third transistor, the source of the third transistor being at a low level and a drain thereof being coupled to an emitter of the fourth transistor, the fourth resistor, and a gate of the fifth transistor respectively, the fourth resistor being at a high level, a base of the fourth transistor being coupled to the detection device for receiving the installation correctness signal therefrom and an emitter thereof being at a low level, a source of the fifth transistor being at a low level

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and a drain thereof being coupled to a drain of the second transistor, the fifth resistor, and an output of the signal selector respectively, and the fifth resistor being at a high level as claimed in Claim 2.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

7. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Kerr teaches active enclosure for a computing device.

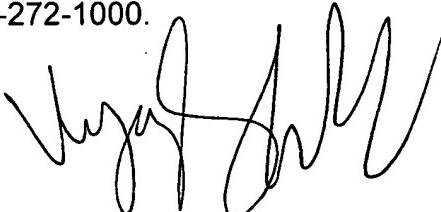
Geheb et al teaches the docking station for a patient monitoring device. .

8. Any inquiry concerning this communication or earlier communications from the examiner should be directed to VIJAY SHANKAR whose telephone number is (571) 272-7682. The examiner can normally be reached on M-F 7:00 am - 4:30 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, BIPIN SHALWALA can be reached on (571) 272-7681. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.



VIJAY SHANKAR
Primary Examiner
Art Unit 2629

VS